

CLAIMS

1. Circuitry for providing data security, which
circuitry contains at least one processor and at least one
5 storage circuit and which circuitry comprises:

at least one storage area in said storage circuit, in
which storage area protected data relating to circuitry
security are located;

mode setting means arranged to set said processor in
10 one of at least two different operating modes, the mode
setting means being capable of altering the processor
operating mode;

storage circuit access control means arranged to
enable said processor to access said storage area in which
15 said protected data are located when a first processor
operating mode is set; and

storage circuit access control means arranged to
prevent said processor from accessing said storage area in
which protected data are located when a second
20 processor operating mode is set, thereby enabling said at
least one processor to execute non-verified software
downloaded into the circuitry.

2. The circuitry for providing data security according
to claim 1, further comprising:

25 a timer arranged to control a time period during
which the processor is in said second operating mode.

3. The circuitry for providing data security according
to claim 1, further comprising:

authentication means arranged to authenticate
30 software provided to the circuitry.

4. The circuitry for providing data security according to claim 1, further comprising:

means arranged to indicate in which mode the processor is operating.

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5. The circuitry for providing data security according to claim 1, wherein said mode setting means comprise an application program.

6. The circuitry for providing data security according to claim 1, which circuitry is comprised in a mobile telecommunication terminal.

7. A method for providing data security in circuitry containing at least one processor and at least one storage circuit, which method comprises the steps of:

15 storing protected data relating to circuitry security in said storage circuit;

setting said processor in one of at least two different alterable operating modes;

enabling said processor to access said storage area in which said protected data are located when a first processor operating mode is set; and

20 preventing said processor from accessing said storage area in which protected data are located when a second processor operating mode is set, thereby enabling said at least one processor to execute non-verified software downloaded into the circuitry.

8. The method for providing data security according to claim 7, further comprising the step of:

controlling a time period during which the processor is in said second operating mode by means of a timer.

9. The method for providing data security according to claim 7, further comprising the step of:
authenticating software provided to the circuitry.

10. The method for providing data security according to claim 7, further comprising the step of:
indicating in which mode the processor *is* operating.

11. The method for providing data security according to claim 7, wherein the setting of said
processor in one of at least two different alterable operating modes is performed by means of an application program.

12. The method for providing data security according to claim 7, wherein the circuitry containing at least one processor and at least one storage circuit is comprised in a mobile telecommunication terminal.